1/10 Austin et al. END920030035US1 RAK

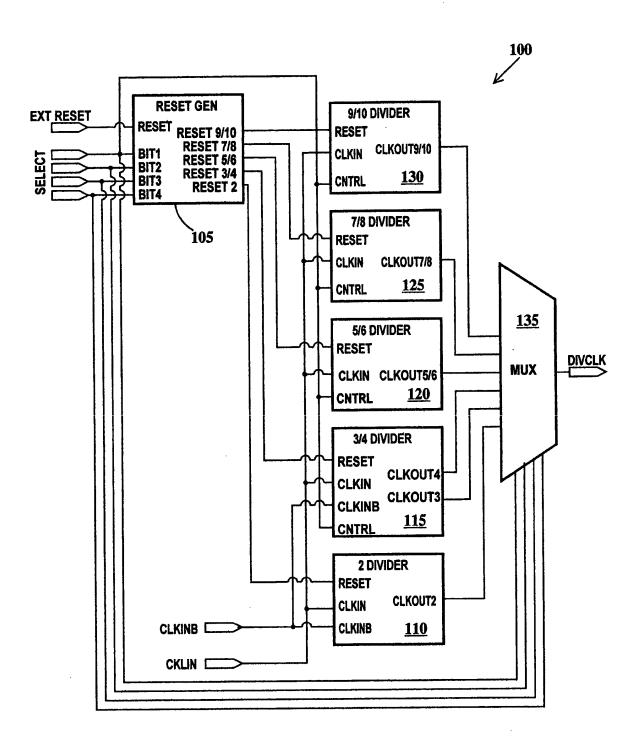
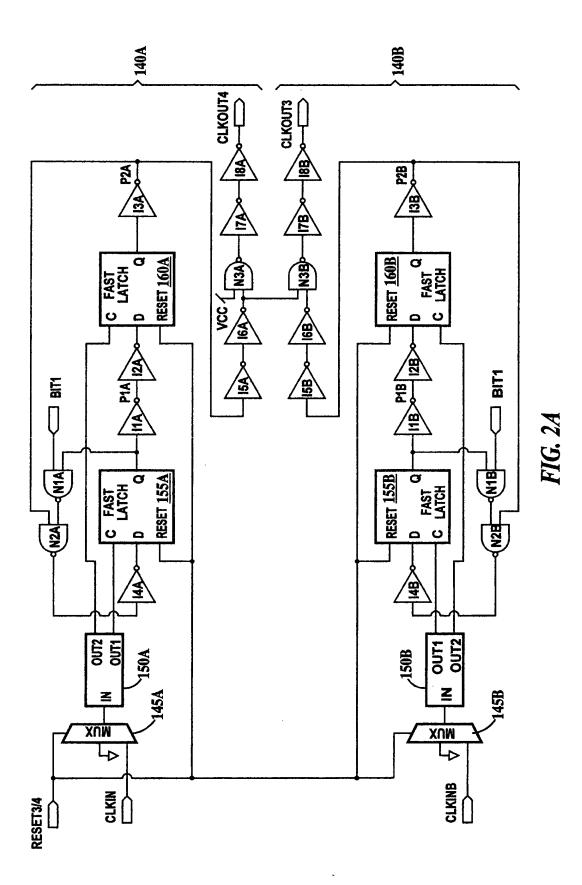


FIG. 1

2/10 END20030035US1



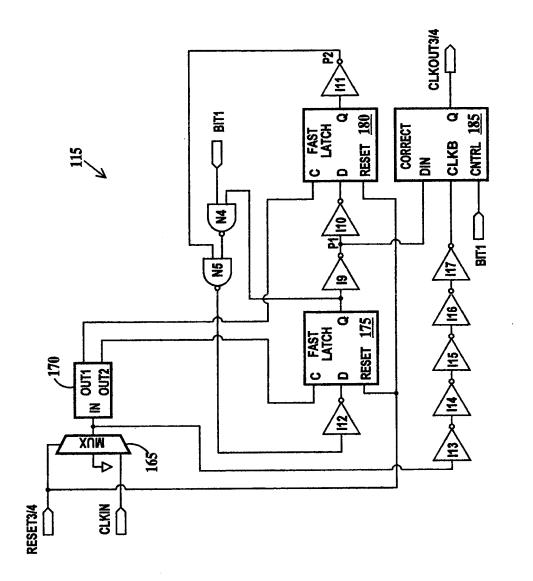


FIG. 2B

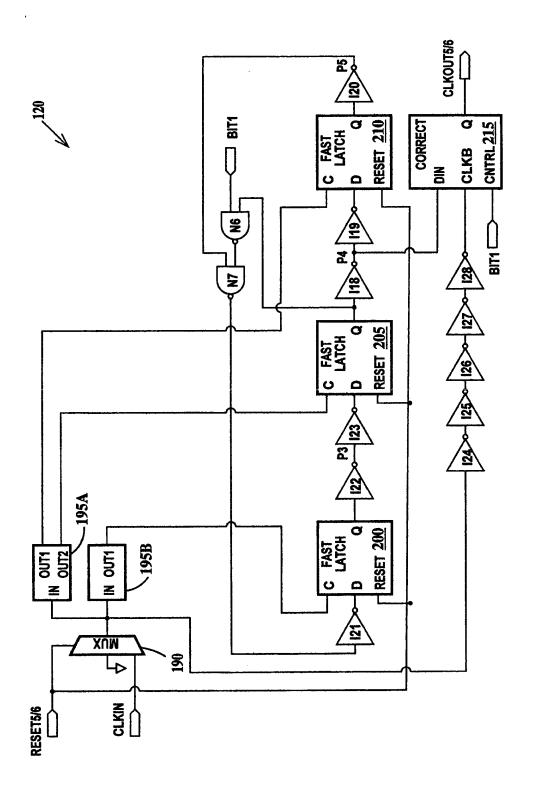


FIG. 3

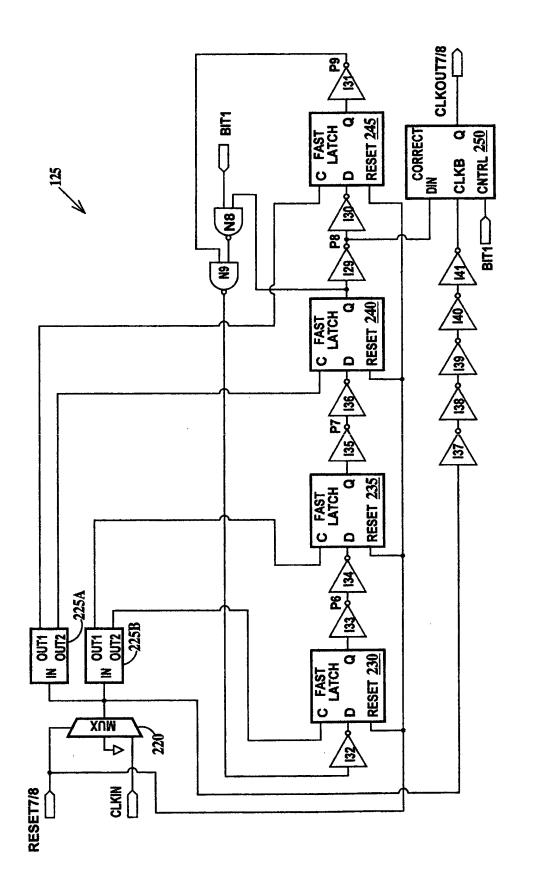


FIG. 4

6/10 END20030035US1 CLKOUT9/10 RESET 285 BIT CLKB Q CORRECT P13 RESET 280 C FAST LATCH D Q <u>2</u> RESET 275 D Q RESET 270 -260A -260C IN OUT? IN OUT1 N OUT1 RESET 265 XUM 255, SKI RESET9/10

FIG. 5

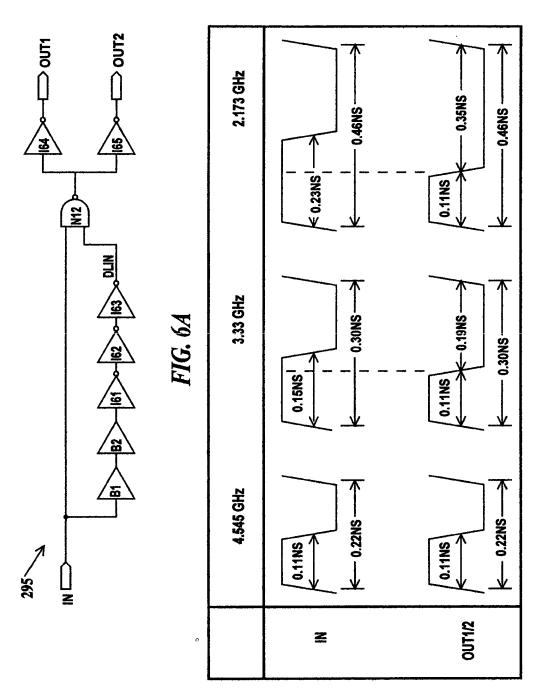


FIG. 6B

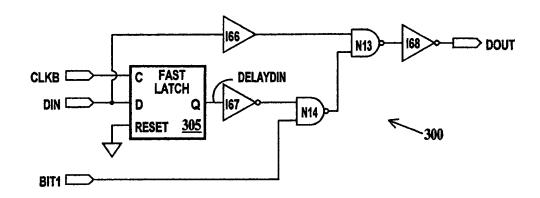


FIG. 7A

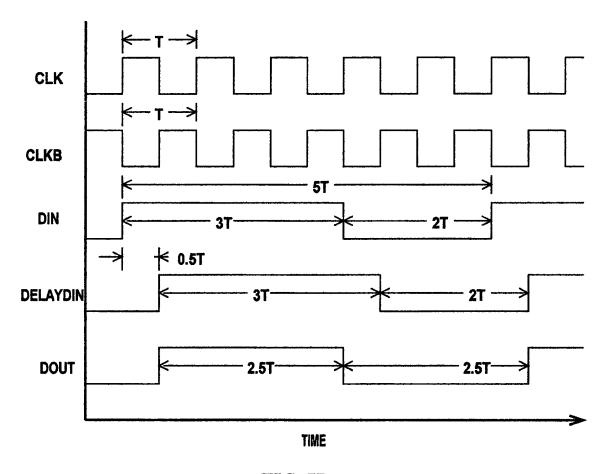
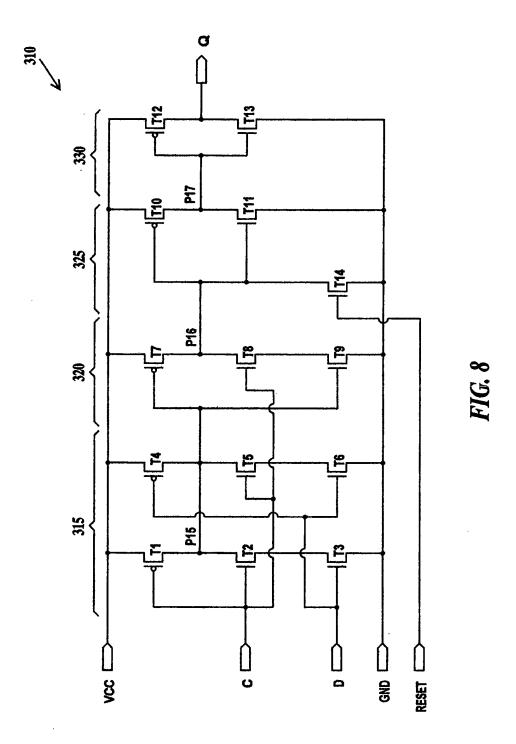


FIG. 7B



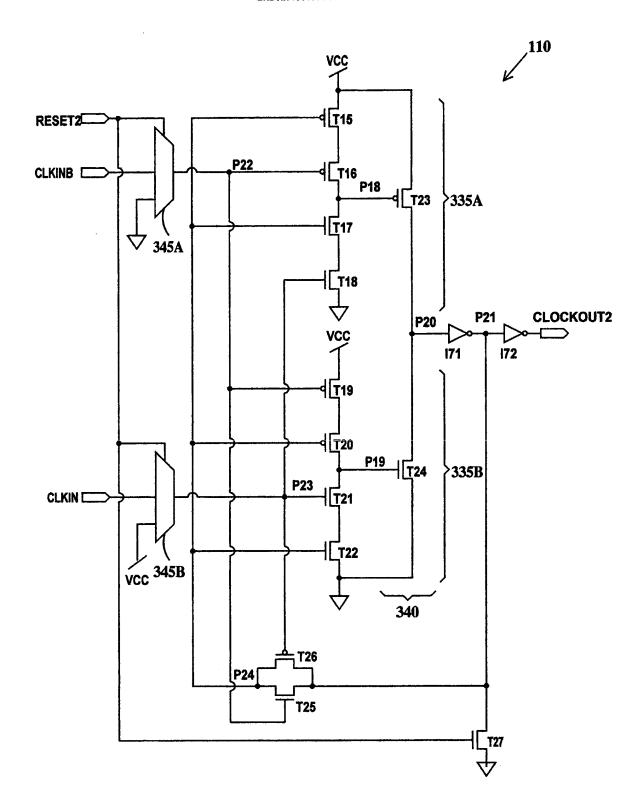


FIG. 9